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Comparative Analysis of Various Multicarrier Modulation Techniques for Different Multilevel Converters

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ABSTRACT

The applications of Multilevel Converter (MLC) are increased because of the huge demand for clean power; especially these types of converters are compatible with the renewable energy sources. In addition, these new types of converters have the capability of high voltage and high power operation. A Nine-level converter in three modes of implementation; Diode Clamped-MLC (DC-MLC), Capacitor Clamped-MLC (CC-MLC), and the Modular Structured-MLC (MS-MLC) are analyzed and simulated in this paper. Various types of Multicarrier Modulation Techniques (MMTs) (Level shifted (LS), and Phase shifted (PS)) are used for operating the proposed Nine level - MLCs. Matlab/Simulink environment is used for the simulation, extracting, and analysis the results. Finally, a comparison is made between the results for all topologies that are implemented regarding to the criteria of the output voltage waveforms harmonic distortion factor, No. of the necessitated power components, and the complexity of each circuit. Based on simulation results, the MS-MLC is finer as compared to the other types of MLCs. It also observed that the MLCs (with three types) using Phase Opposition Disposition (POD) technique is performed better in terms of getting greater fundamental output voltage and lower THD% as compared to the other techniques.

Keywords: MLC, MMTs, LS, and PS.

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مقارنة بين اداء تقنيات التضمين المتعددة النواقل لجميع انواع مغيرات القدرة متعددة المستويات

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الخلاصة

في السنوات القليلة الماضية ازداد الاقبال والتوجه نحو تطبيقات مغيرات القدرة متعددة المستويات MLCs بسبب الحاجة الماسة للطاقة النظيفة. خصوصا وان هذه الانواع من المغيرات تعمل او تتوافق مع مصادر الطاقات المتجددة. بالاضافة الى المستمر المكانية عملها بقدرات وفولتيات عالية لانها تقوم بتوليف موجة اخراج التيار المتناوب المطلوبة من عدة مصادر للتيار المستمر دون الحاجة الى استخدام مفاتيح تتحمل الجهد العالي. حيث تم تمثيل ومحاكاة مغير فولتية ذو تسع مستويات المقترح وبالاشكال ون الحاجة الى استخدام مفاتيح تتحمل الجهد العالي. حيث تم تمثيل ومحاكاة مغير فولتية ذو تسع مستويات المقترح وبالاشكال دون الحاجة الى استخدام مفاتيح تتحمل الجهد العالي. حيث تم تمثيل ومحاكاة مغير فولتية ذو تسع مستويات المقترح وبالاشكال دون الحاجة الى استخدام مفاتيح تتحمل الجهد العالي. حيث تم تمثيل ومحاكاة مغير فولتية ذو تسع مستويات المقترح وبالاشكال جميع هذه الالاثة لتكوين مغير القدرة متعدد المستويات وهي : DC-MLC, CC-MLC, MS-MLC تمت السيطرة على جميع هذه الانواع من مغيرات القدرة باستخدام مختلف تقنيات التضمين المتعددة النواقل MMTs والتي تتضمن طريقتي : تتضمن الموجة الماراحة عموديا كاو و تقنية تضمين المتعددة النواقل MMTs والتي تتضمن طريقتي : تقنيات تضمين الموجة الناوقل من مغيرات القدرة باستخدام مختلف تقنيات التضمين الموجة الناوقا من مغيرات القدرة باستخدام مختلف تقنيات التضمين الموجة الناقلة المزاحة أفقيا (طوريا) SP . تم أعتماد برنامج المحاكاة المزاحة عموديا كاو و تقنية تضمين الموجة الناقلة المزاحة أفقيا و لجميع طرق التربي الم الميزام الموجة الناوقا من علي و لجميع طرق السيطرة التي ذكرت. ولمعرفة النوع الامثل تمت مقارنة النتائج على اساس معايير جودة الجهد الناتج من ناحية القدرة السيطرة التي ذكرت. ولمعرفة العظمى لفولتية الاخراج بالاضافة الى الموانية الماس عدد مكونات القدرة السوافقيات القدرة الموقية الى ألمولوبة في كل نوع من انواع تمثيل مغيرات القدرة الثلاث.

الكلمات الرئيسية: مُغير القدرة متعدد المستويات، تقنيات التضمين متعددة النواقل، تضمين الموجة المزاحة عموديا، تضمين الموجة المزاحة أفقيا

1. INTRODUCTION

The necessity to work switching power converters in huge power usage has forced the advancement of multilevel conversion arrangements. This is acutely for the reason that ability of MLCs to operate at high voltages and then to synthesize as waveforms of excellent spectral quality. The development of MLC technology has conformed two parallel lanes, the first treating with the converter topologies themselves, and the another dealing with the enhancement of modulation control strategies suitable for the emerging topologies, McGrath, and Holmes, 2012 and this paper deals seriously with these two parallel paths. Numerous modulation controller strategies are used for operating the MLCs. They can generally be classified into three categories as in Kouro, et al., 2008, Manimala, et al., 2011, and Calais, et al., 2001: Selective Harmonic Elimination (SHE) which synthesize the ac voltage by utilizing pre-evaluated switching angles with fundamental switching frequency, Space Vector Strategies (SVS) which have been applied to 3-phase MLC applications, and MMTs or can be known as "Carrier-based modulation techniques", which are a very accepted method in industrial usages. This paper focuses on all types of topology for MLC, unlike the researchers of Venka, et al., 2014, Dubey, et al., 2016 whereas the researchers focus on MS-MLCs, while authors Rane, et al., 2014 simulate only DC-MLC topology, and finally in Thielemans, et al., 2009, the researcher focuses on CC-MLC. Our work involves an analysis and simulation of nine-level MLC (in three types of topology) with all types of MMTs. A complete comparison between them regarding to the criteria of output voltage quality (fundamental peak voltage, Total Harmonic Distortion (THDV) of the output voltage) has been done. In addition to the comparison of the number of the power component requirements per phase leg among three MLCs.

2. MULTILEVEL CONVERTER TOPOLOGIES

The MLCs are basically classified into three forms in its structure. All the topologies have the similar property of dropping the harmonics. MLCs have many attractive features (Compared



with common two level converters) that is: ability of working with high voltage, reduced common mode voltages, lower switching voltage stress, upgrades the output voltage waveform, and smaller or even no output filter is required. The limitation of the MLC is that it required a great amount of semiconductors component, **Kouro, et al., 2008, Aswini, et al., 2017.**

2.1 Diode Clamped Multilevel Converter

This type of converter uses single dc voltage source that is partitioned into a numeral of voltage steps by the chain sequence of capacitors known as "dc side capacitors". The purpose of clamping diodes in the circuit is to avoid the voltage through one of the switching power devices which exceed the voltage across one of the capacitors, **Rane, et al., 2014**, and **Seyezhai, et al., 2008**. The output voltage of an L-level MLC equivalent to the following equation

$$L = 2L' - 1 \tag{1}$$

Where: L' is the number of the levels of the output voltage per phase leg.

As in this work the 9-level (line voltage converter (L)), has a 5-levels (per phase level converter (L')).

To produce L'-level staircase output phase voltage: Common (L' - 1) dc side capacitors are needed, (L' - 1) * (L' - 2) clamping diodes it should be used, 2 * (L' - 1) main switching devices are needed for each phase leg. Thus three of dc side capacitors (C1, C2, and C3), six of clamping diodes in each phase leg, and six of power switch and its parallel diodes are used in the structure of a 7-level DC-MLC (L' = 4) is shown in **Fig. 1-a**, **Nami, et al., 2008**.

2.2 Capacitor Clamped Multilevel Converter

This converter is identical to DC-MLC in their structure, excepting of using clamping capacitors in place of clamping diodes as shown in **Fig. 1- b**; circuit diagram of a seven-level (L' = 4) CC-MLC. To generate L'- level staircase output phase voltage: (L' - 1) dc link capacitors are needed. (L' - 1) * (L' - 2) / 2 clamping capacitors are required for each phase leg (as C_{a1}, C_{a2}, and C_{a3}). Each phase leg of the converter should contains of 2 * (L' - 1) main switching devices and its parallel diodes. The major problem of these types of converters is that it requires most number of capacitor comparative to other MLC, causing to arise difficulties of the packaging and huge weight, **Thielemans, et al., 2009.**

2.3 Modular Structured Multilevel Converter

This type includes a chain of H- Bridge converter modules. The main concept of this type of converters is to synthesize the required ac output voltage from numerous separate dc input sources, **Manimala, et al., 2011**. The MS-MLC is well appropriate for renewable energy applications because naturally of offering isolated dc sources: as Photovoltaic (PV), Aswini, et al., 2017, rectified output of Wind turbine generator as in paper of Samue, et al., 2010, Fuel Cell, Seyezhai, et al., 2008, in addition to in some paper a hybrid renewable power sources were used for MLC as a hybrid (PV plus Wind) as in research of Ganesh, et al., 2014.

The number of levels varies with the number of H - Bridge Module (M) which is equal to the number of dc source (E_M), the expression can be written as (L = 2M + 1). So, the number of L can be easily controlled by attaching or eliminating the M, as shown below in **Fig. 2**, **Zainal**, et al., 2009, and Charai, et al., 2017.

MS-MLC owns several distinctions over other MLC topologies in terms of circuit arrangement flexibility, and casing is possible because each level reiterated with the same structure. Furthermore, it does not suffer the voltage-unbalancing problem as experienced by the DC-MLC



(its average) and in CC-MLC (it's high). MS-MLC is more convenient than other MLC because it doesn't have any clamping diode and clamping capacitor, **Aswini, et al., 2017.**

3. MULTICARRIER MODULATION TECHNIQUES

The power electronic switches of MLC are mainly controlled with MMTs. MMTs can be divided into two types: LS- Pulse Duration Modulation (PDM) methods, where several carriers are level (Vertically) shifted, and PS-PDM method, where several carriers are phase (Horizontally) shifted accordingly see **Fig. 3**, **Calais, et al., 2001**, and **Venka, et al., 2014**.

3.1 Level Shifted Techniques (Carriers Shifted Vertically)

This type is also called "Carrier Disposition (CD) techniques". For an *L*- level converter, L - 1 carriers with identical frequency and identical peak to peak amplitude are arranged such that the groups they occupy are adjacent. Unique reference waveform is needed and it is put in the center of the carrier set. The reference is always equated with all of the carrier signals. If the reference is greater than the carrier signal, the active device that corresponds to that carrier is switched on and vice versa, **Malathy** and **Ali**, **2012**. LS-PDM can be categorized into three groups: Phase Disposition (PD), POD, and Alternative Phase Opposition Disposition (APOD) **Reddy, et al., 2010**, and **Angulo, et al., 2007**:

3.1.1 Phase Disposition

All carriers are in phase as appeared in **Fig. 3-a**. For this method, major harmonic power is focused at the carrier frequency.

3.1.2 Phase Opposition Disposition

All carriers higher than the zero reference value are out of phase with those lower the zero reference value by 180° as shown in **Fig. 3-b**.

3.1.3 Alternative Phase Opposition Disposition

All the carriers are replacement in position (i.e. "each carrier is level shifted by 180° from its next carriers") as shown in **Fig. 3-c**. For this technique, the greatest harmonics are sidebands centered on the carrier frequency with negative harmonics occurring directly on the carrier frequency.

3.2 Phase Shifted Technique (Carriers Shifted Horizontally)

Normally, PS-PWM is used with MS-MLCs and CC-MLCs. In this technique (L - 1)/2 carriers are used for a L-level converter, all carriers with same magnitude and frequency but phase shifted by $180^{\circ}/((L - 1)/2)$. There are two of the opposite reference signals (phase shifted by 180° to each other) are required for the two phase legs of the MLC converter, one for a half leg and the inverted signal for the other half leg as shown in **Fig. 3**. The gate signals are a result of the comparison between carrier wave and reference signal, **McGrath**, and **Holmes**, 2012.

4. SIMULATION CIRCUITS

To verify the proposed schemes, a simulation model for nine-level converters is implemented using Matlab/Simulink simulation tool as shown in **Fig. 4**. MOSFET is chosen as the main power switch for all types of MLCs. Simulation parameters of the proposed MLCs are shown in **Table 1**. To make a compression between the three circuit diagrams of 9-level MLCs in criteria



of complexity, **Table 2** lists the summary of the needed number of the power electronic element to implements theses circuits.

For all the configurations of MMTs, simulations are done by varying the (subsystem 1) as shown in **Fig. 5** which presents a simulation circuit of LS-PDM technique (PD method); for implementing the LS-PDM for nine-level MLC, eight carrier signals with the same peak to peak and same switching frequency are required. These eight signals are compared with single reference sine wave to produce eight signals for the first leg of the converters and then are negated to produce the second eight signals for the second leg of the converters.

Fig. 6 shows simulation circuit of PS-PDM technique; four of carrier signals are required to implement this technique then a single carrier signal is used and phase shifted three times with 45° by using (three of Transport Delay block) to produce the another three carrier signals.

5. SIMULATION RESULTS

The output voltage and its Harmonic Spectrum (HS) with various MMTs of nine-MLC are shown in the Figures below as following :

Fig.7 shows the results of DC-MLC, Fig. 8 shows the results of CC-MLC, and Fig. 9 shows the results of MS-MLC.

The PS-PDM technique isn't suitable with DC-MLC, where the THD amplitude is very high therefore it is not implemented in the study.

After presenting the output voltage waveforms and its HS of the converters, we make a summary table of the peak amplitude of the fundamental output voltage, and its THD% of nine-level MLCs for every MMT for easily compression among them, as listed in **Table 3**.

From **Table 3** we conclude that the output of MLCs with POD technique (in each one of three types of MLCs) has a lower amplitude of THD% as compared with the other techniques of LS-PDM (PD, and APOD) and PS-PDM .

It is also seen that MS-MLC with MMTs is better since it provides relatively superior fundamental output voltage and minimal output THD_V% compared with the others MLCs types. In addition to the MS-MLC requires the minimum number of power electronic components as compared with two other kinds to achieve the same output voltage levels as shown in **Table 2**.

The alone drawback of MS-MLC, it's demanded separately dc sources, but this drawback can be considered as an advantage as its utilization of a great number of dc sources allows for the converter to generate high voltages and thus high power ratings.

6. CONCLUSIONS

Nine-level MLC with three types (DC, CC, and MS) employing different MMTs has been implemented and analyzed. It can be concluded from the results above the following points :

- The harmonics in the output voltage waveform was reduced significantly by applying the different configuration of MMTs especially LS-PDM techniques and as a result there is no need to a high necessity filters.
- In all topologies of MLCs, the harmonic content is proportionate with the numbers of level of the converter, so it's possible to improvement the harmonic content by increasing the levels of the converter.



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8. LIST OF SYMBOLS

APOD = Alternative Phase Disposition			
CC-MLC = Capacitor Clamped-Multilevel Converter			
CD = Carrier Disposition			
DC-MLC = Diode Clamped-Multilevel Converter			
HS = Harmonic Spectrum			
LS = Level shifted			
MLC = Multilevel Converter			
MMTs = Multicarrier Modulation Techniques			
MS-MLC = Modular Structured -Multilevel Converter			
PD = Phase Disposition			
PDM = Pulse Duration Modulation			
POD = Phase Opposition Disposition			
PS = Phase shifted			
PV = Photovoltaic			
SHE = Selective Harmonic Elimination			
SVS = Space Vector Strategy			
THD = Total Harmonic Distortion			

Table	1.	Simulation	parameters.
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Parameters	Value
dc voltage source	100 V
Switching frequency	5 KHz
Fundamental frequency	50 Hz
R load	100 Ω
L load	20 mH



Type of MLC	DC	CC	MS
Main power switches	8	8	8
Main diodes (Parallel Diodes)	8	8	8
Clamping diodes	12	-	-
Clamping capacitors	-	6	-
dc - link capacitors	4	4	0
No. of dc source	1	1	4

	Table 2. Major el	ement requirements	for 9-level ML	C types	per phase leg	g.
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Table 3. Comparison of Vpeak, and THD_V% for various MMTs for three types of MLC.

Type of MLC	Type of MMTs		Fundamental Voltage (V)	THDv%
		PD	398.4	13.88
DC-MI C	I S-PDM	POD	398.7	13.85
DC-IVILC	LS-PDM	APOD	398.4	13.91
		PD	398.4	13.92
CC-MLC	LS-PDM	POD	398.6	13.89
		APOD	398.3	13.95
	PS-PDM		395	14.98
		PD	399.7	13.80
MS-MLC	LS-PDM	POD	399.9	13.77
		APOD	399.6	13.84
	PS -P	'DM	395	14.98



(a) DC-MLC (b) CC-MLC Figure 1. Circuit diagram of seven-level MLC.





Figure 2. Circuit diagram of MS-MLC.



Figure 3. MMTs for MLC.





(a) DC-MLC.



(b) CC-MLC.





(c) MS-MLC. Figure 4. Simulation circuits of nine-level: (a) DC-MLC, (b) CC-MLC, and (c) MS-MLC.





Figure 5. LS-PDM technique (PD).

Figure 6. PS-PDM technique.



Figure 7. Output voltage and the HS of nine-level DC-MLC with various MMTs: (a): PD, (b): POD, and (c): APOD.



Figure 8. Output voltage and the HS of nine-level CC-MLC with various MMTs: (a): PD, (b): POD, (c): APOD, and (d) PS-PDM.



Figure 9. The output voltage and the HS of 9-level MS-MLC with various MMTs: (a): PD, (b): POD, (c): APOD, and (d): PS-PDM.